

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William J. Dally, Philip P. Carvey, Larry R. Dennison and P. Allen King

Continuation Application of

Application No.: 09/887,960

Filed: June 22, 2001

For: Methods and Apparatus for Event-Driven Routing

Date: <u>9.29.03</u>
EXPRESS MAIL LABEL NO. <u>EV215729405 US</u>

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.129(a), or  
(First/Second submission after Final Rejection)
- ☒ under 37 CFR 1.97(b), or  
(Within any one of the following time periods: three months of filing national application (other than a CPA) or date of entry of the national stage in an international application; or before the mailing date of a first office action on the merits in a non-provisional application, including a CPA, or a Request for Continued Examination).
- ☐ under 37 CFR 1.97(c) together with either:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, or
- ☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the 37 CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97(d) together with:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, and
- ☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(Filed after final action or notice of allowance, whichever occurs first, but on or before payment of the issue fee)
- ☐ under 37 CFR 1.97(i):  
Applicant requests that the IDS and cited reference(s) be placed in the application filewrapper.  
(Filed after payment of issue fee)

Statement Under 37 CFR 1.97(e)

- ☐ Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement; or
- ☐ No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Statement Under 37 CFR 1.704(d) (Patent Term Adjustment)

Applies to original applications (other than design) filed on or after May 29, 2000

- ☐ Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of the Information Disclosure Statement.
- ☒ Enclosed herewith is form PTO-1449:
- ☐ Copies of the cited references are enclosed.
- ☐ Since this application was filed after June 30, 2003, copies of issued U.S. patents and published U.S. applications are not required and are not being provided.
- ☒ Copies of the cited references are enclosed except those entered in prior applications, U.S. Application Nos. 09/084,636 and 09/887,960, to which priority under 35 U.S.C. 120 is claimed. [The earlier application contains copies of the cited references.]
- ☐ The listed references were cited in the enclosed International Search Report in a counterpart foreign application.
- ☐ The "concise explanation" requirement (non-English references) for reference(s) [ ] under 37 CFR 1.98(a)(3) is satisfied by:
- ☐ the explanation provided on the attached sheet.
- ☐ the explanation provided in the Specification.
- ☐ submission of the enclosed International Search Report.
- ☐ submission of the enclosed English-language version of a foreign Search Report and/or foreign Office Action.
- ☐ the enclosed English language abstract.

☐ Applicant requests that the following non-published pending applications be considered:

Examiner's  
Initials

\_\_\_\_ U.S. Patent Application No. [ ], by [inventor(s)], filed [ ], Docket No.: [ ]  
\_\_\_\_ U.S. Patent Application No. [ ], by [inventor(s)], filed [ ], Docket No.: [ ]  
\_\_\_\_ U.S. Patent Application No. [ ], by [inventor(s)], filed [ ], Docket No.: [ ]

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Examiner

\_\_\_\_\_  
Date

- ☐ A copy of each above-cited application, including the current claims, is enclosed.
- ☐ A copy of each above-cited application, including the current claims, is enclosed, except those entered in prior application, U.S. Application No. [ ], to which priority under 35 U.S.C. 120 is claimed.

The Examiner is requested to return a copy of the above list of pending applications indicating which references were considered with the next office communication.

It is requested that the information disclosed herein be made of record in this application.


Method of payment:

- ☐ A check for the fee noted above is enclosed, or the fee has been included in the check with the accompanying Reply. A copy of this Statement is enclosed.
- ☐ Please charge Deposit Account 08-0380 in the amount of \$[ ]. A copy of this Statement is enclosed.
- ☒ Please charge any deficiency in fees and credit any overpayment to Deposit Account 08-0380.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By

  
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James M. Smith  
Registration No.: 28,043  
Telephone: (978) 341-0036  
Facsimile: (978) 341-0136

Concord, MA 01742-9133

Dated: 9/29/13

PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>September 26, 2003</b>  (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2390.1001-012		CONT. OF APPLICATION NO. 09/887,960	
	APPLICANT William J. Dally, <i>et al.</i>			
	FILING DATE	CONFIRMATION NO.	GROUP	

## U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER	ISSUE DATE / PUBLICATION DATE	NAME
	AA	4,118,094	10/03/78	Key
	AB	4,340,092	07/20/82	Chisholm
	AC	4,371,013	02/01/83	Camp
	AD	4,469,388	09/04/84	Narozny
	AE	4,495,615	01/22/85	Wilcke
	AF	4,631,637	12/23/86	Romania et al.
	AG	4,861,274	08/29/89	Brune et al.
	AH	4,862,451	08/29/89	Closs et al.
	AI	4,911,645	03/27/90	August et al.
	AJ	4,933,933	06/12/90	Dally et al.
	AK	5,088,091	02/11/92	Schroeder et al.
	AA2	5,123,848	06/23/92	August et al.
	AB2	5,134,690	07/28/92	Samatham
	AC2	5,144,691	09/01/92	August et al.
	AD2	5,155,784	10/13/92	Knott
	AE2	5,172,371	12/15/92	Eng et al.
	AF2	5,179,558	01/12/93	Thacker et al.
	AG2	5,261,827	11/16/93	Lenzi et al.
	AH2	5,355,372	10/11/94	Sengupta et al.
	AI2	5,408,469	04/18/95	Opher et al.
	AJ2	5,444,701	08/22/95	Cypher et al.
	AK2	5,425,169	06/20/95	Steinman et al.
	AA3	5,513,172	04/30/96	Shikama et al.
	AB3	5,521,591	05/28/96	Arora et al.
	AC3	5,532,856	07/02/96	Li et al.
	AD3	5,581,705	12/03/96	Passint et al.

EXAMINER	DATE CONSIDERED
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### FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
	AL	0 886 454 A2	12-23-98	Europe		
	AM					
	AN					
	AO					
	AP					
	AQ					
	AL2					
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	AO4					
	AP4					
	AQ4					
	AL5					
	AM5					

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
AR	Glass, C.J., and Ni, L.M., "The Turn Model for Adaptive Routing," Proc. 19th Internat. Symposium on Computer Architecture, 278-287 (May 1992).	
AS	Rettberg, R., et al., "Development of a Voice Funnel System: Design Report," Bolt Beranek and Newman, Inc., Report No. 4098, 149 pages (August 1979).	
AT	Dally, William, "Network and Processor Architecture for Message-Driven Computers." In VLSI and Parallel Computation, Suaya, R., and Birtwistle, G., eds. (San Mateo, CA:Morgan Kaufman), pp. 140-222 (1990).	
AU	Dally, William J., "Virtual-Channel Flow Control," IEEE Transactions on Parallel and Distributed Systems, 3(2):194-205 (March 1992).	
AV	Dally, W.J., and Seitz, C.L., "The torus routing chip," Distributed Computing, 1:187-196 (1986).	
AW	Jesshope, Chris, "The MP1 Chip and its Application," Dept. of Electronic and Electrical Engineering, Univ. of Surrey, Guildford, Surrey GU2 5XH, UK, 47-54.	
AX	Kessler, R.E., and Schwarzmeier, J.L., "CRAY T3D: A New Dimension for Cray Research," IEEE, 1063-6390/93, 176-182 (1993).	
AY	Dally, William <i>et al.</i> , "Deadlock-Free Message Routing in Multiprocessor Interconnection Networks," IEEE Transactions on Computers, C-36(5):547-553 (May 1987).	
AZ	Jesshope, C., and Izu, C., "The MP1 Network Chip," IEEE, 1066-6192/92, 338-348 (1992).	
AR2	Dally, William J., et al., "The Reliable Router: A Reliable and High-Performance Communication Substrate for Parallel Computers," Proc. of Parallel Computer Routing and Communication Workshop, Seattle, WA, 241-255 (May 1994).	
AS2	Dally, W.J., et al., "Architecture and Implementation of the Reliable Router," Proc. of Hot Interconnects II, Stanford, CA, 122-133 (August 11-13, 1994).	
AT2	Nuth, Peter et al., "The J-Machine Network," Proc. of the Internat. Conference on Computer Design: VLSI in Computers and Processors, Cambridge, MA, 420-423 (October 1992).	
AU2	Dennison, Larry R., "The Reliable Router: An Architecture for Fault Tolerant Interconnect," doctoral dissertation, Massachusetts Institute of Technology, Cambridge, MA, 444 pages (June 1996).	

EXAMINER	DATE CONSIDERED
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